



- 1 -

Docket: 0756-1650

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NR

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Rose M. Fichtel

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

|                              |                              |
|------------------------------|------------------------------|
| In re PATENT Application of: | )                            |
| Shunpei YAMAZAKI et al.      | ) Allowed: February 16, 1999 |
| Serial No. 08/847,314        | ) ISSUE BRANCH -             |
| Filed: April 23, 1997        | ) Batch #P19                 |
| For: LASER PROCESSING        | )                            |
| METHOD, METHOD FOR           | )                            |
| FORMING A FLASH              | )                            |
| MEMORY, INSULATED            | )                            |
| GATE SEMICONDUCTOR           | )                            |
| DEVICE AND METHOD FOR        | )                            |
| FORMING THE SAME             | )                            |

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SUBMISSION OF FORMAL DRAWINGS  
IN RESPONSE TO NOTICE OF ALLOWABILITY

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

In response to the Notice of Allowability dated February 16, 1999, submitted herewith are 8 sheets of formal drawings, consisting of Figures 1-10 for filing in the subject application.

Respectfully submitted,

Eric J. Robinson

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